

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An electro-optical panel, comprising:  
a plurality of scanning lines;  
a plurality of data lines that intersect the scanning lines;  
pixels disposed in a matrix form so as to correspond to intersections of the scanning lines and the data lines, each pixel including a switching element and a pixel electrode;  
a pixel area including the pixels and having an effective pixel area that contributes to image display and a dummy pixel area that does not contribute to image display; and  
an identification pattern representing identification information that identifies the respective scanning lines, at least a part of the identification pattern being formed in the dummy pixel area to overlap with the pixel electrode of the dummy area between the adjacent scanning lines.
2. (Original) The electro-optical panel according to Claim 1, the dummy pixel area having a first area provided on one end of the respective scanning lines, and a second area provided on an other end thereof,  
the identification pattern being formed by being divided in the first area and the second area.
3. (Currently Amended) An electro-optical panel, comprising:  
a plurality of scanning lines;  
a plurality of data lines that intersect the scanning lines;

pixels disposed in a matrix form so as to correspond to intersections of the scanning lines and the data lines, each pixel including a switching element and a pixel electrode;

a pixel area including the pixels and having an effective pixel area that contributes to image display and a dummy pixel area that does not contribute to image display; and

an identification pattern representing identification information that identifies the respective data lines, at least a part of the identification pattern being formed in the dummy pixel area to overlap with the pixel electrode of the dummy pixel area between the adjacent data lines.

4. (Original) The electro-optical panel according to Claim 3, the dummy pixel area having a first area provided on one end of the respective data lines, and a second area provided on an other end thereof,

the identification pattern being formed by being divided in the first area and the second area.

5. (Original) The electro-optical panel according to Claim 1, the switching element being connected to a pixel electrode, and the identification pattern being smaller than the pixel electrode.

6. (Original) The electro-optic panel according to Claim 1, further including an element substrate and a counter substrate opposing the element substrate,

the plurality of scanning lines, the plurality of data lines, and the respective switching elements being formed on the element substrate, and a scanning line driving circuit to drive the respective scanning lines and a data line driving circuit to drive the respective data lines being formed in an area that does not overlap the identification pattern.

7. (Original) The electro-optic panel according to Claim 1, further including an element substrate and a counter substrate opposing the element substrate,

the plurality of scanning lines, the plurality of data lines, and the respective switching elements being formed on the element substrate, and

the identification pattern being formed on the counter substrate.

8. (Original) The electro-optic panel according to Claim 1, the identification pattern being constituted by material that shields light.

9. (Original) The electro-optic panel according to Claim 1, the identification pattern being constituted by material that reflects light.

10. (Original) A manufacturing method of an electro-optic panel that includes a plurality of scanning lines, a plurality of data lines, and transistor elements disposed in a matrix form so as to correspond to intersections of the scanning lines and the data lines, and respective pixel areas, partitioned by the respective scanning lines and the respective data lines, having an effective pixel area that contributes to image display and a dummy pixel area that does not contribute to image display, the method comprising:

forming a semiconductive layer on a substrate;

forming a gate insulative film on the semiconductive layer; and

forming an identification pattern to identify at least one of the scanning lines and the data lines in at least a part of the dummy pixel area, concurrently with forming a gate of the transistor element on the gate insulative film.

11. (Original) An electronic instrument, comprising:

the electro-optic panel according to Claim 1.